



PATENT Customer No. 22,852 Attorney Docket No. 04028.0235

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	
Tadao ISHIBASHI et al.) Group Art Unit: 2874
Application No.: 10/574,513) Examiner: Smith, Chad
§ 371(c) Filing Date: November 21, 2006	Confirmation No.: 6977
For: SEMICONDUCTOR OPTOELECTRONIC WAVEGUIDE	

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

In a restriction requirement dated April 29, 2008, the Examiner required restriction under 35 U.S.C. § 121 between the following groups:

Group I:

Claims 15-37, which the Examiner characterized as drawn to a second semiconductor clad layer arranged on each side of one main surface and the other main surface of a semiconductor core layer.

Group II

Claims 38-49, which the Examiner characterized as drawn to a first and second semiconductor clad layers which vertically hold the semiconductor core layer.

Applicants provisionally elect to prosecute Group I, claims 15-37, which the Examiner characterized as drawn to a second semiconductor clad layer arranged on each side of one main surface and the other main surface of a semiconductor core layer.

Please grant any extensions of time required to enter this response and charge any additional required fees to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GABRETT & DUNNER, L.L.P.

Dated: May 29, 2008

By:_

John M. Romary

Reg. No. 26,331 (202) 408-4000